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<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)				ATTY. DOCKET NO. 67161-086		SERIAL NO.  10/646,823	
				APPLICANT Koichi ISHIMI			
				FILING DATE August 25, 2003		GROUP 2816	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
HLN ↓		US 5,687,202	11/11/1997	Eitheim			
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
HLN HLN HLN		JP-11-340823	12/10/1999	NEC CORP.		JAPAN (w/English Abstract)	
		JP2000-244309A	09/08/2000	MITSUBISHI ELECTRIC CORP.		JAPAN (w/English Abstract)	
		PCT/WO85/02275	05/23/1985	MOTOROLA INC.			
<b>OTHER ART (Including Author, Title, Date, Patent, Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
HLN		Kouichi Ishimi et al., "A Full-Digital PLL for Low Voltage LSIs", Technical Report of IEICE, The Institute of Electronics, Information and Communication Engineers, ED97-45, SDM97-23, ICD97-35, June 1997, pp. 29-36					
HLN		Michel Cmohes et al., "A Portable Clock Multiplier Generator Using Digital CMOS Standard Cells" IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 31, NO. 7, July 1996, PP. 958-965					
EXAMINER HAI L. NGUYEN				DATE CONSIDERED 10/25/2004			

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